

SIEMENS

Product data sheet

6ES7134-4JD00-0AB0

SIMATIC DP,
ELECTRONIC MODULE FOR ET 200S, 4 AI TC,
+/-80mV;
15 MM WIDE,
15 BIT + SIGN WITH LED SF (GROUP FAULT).

Supply voltage	
Load voltage L+	
Rated value (DC)	24 V ; From power module
Reverse polarity protection	Yes
Input current	
from load voltage L+ (without load), max.	30 mA
from backplane bus 3.3 V DC, max.	10 mA
Power losses	
Power loss, typ.	0.6 W
Address area	
Address space per module	
Address space per module, max.	8 byte
Analog inputs	
Number of analog inputs	4
permissible input voltage for voltage input (destruction limit), max.	10 V ; Permanent
Cycle time (all channels) max.	Number of active channels per module x basic conversion time
Technical unit for temperature measurement adjustable	No
Input ranges	
Voltage	Yes
Thermocouple	Yes
Input ranges (rated values), voltages	
-80 mV to +80 mV	Yes

Input resistance (-80 mV to +80 mV)	1 MΩ
Input ranges (rated values), thermoelements	
Type B	Yes
Input resistance (Type B)	1 MΩ
Type E	Yes
Input resistance (Type E)	1 MΩ
Type J	Yes
Input resistance (type J)	1 MΩ
Type K	Yes
Input resistance (Type K)	1 MΩ
Type L	Yes
Input resistance (Type L)	1 MΩ
Type N	Yes
Input resistance (Type N)	1 MΩ
Type R	Yes
Input resistance (Type R)	1 MΩ
Type S	Yes
Input resistance (Type S)	1 MΩ
Type T	Yes
Input resistance (Type T)	1 MΩ
Thermocouple (TC)	
Temperature compensation	
internal temperature compensation	Not possible
external temperature compensation with compensations socket	Yes ; possible, one external compensating box per channel
Characteristic linearization	
Parameterizable	Yes ; Type B, E, J, K, L, N, R, S, T to IEC 584
Cable length	
Cable length, shielded, max.	50 m
Analog value creation	
Measurement principle	integrating
Integrations and conversion time/ resolution per channel	
Resolution with overrange (bit including sign), max.	16 bit ; 15 bits + sign

Integration time, parameterizable	Yes
Integration time, ms	16.7 / 20 ms
Interference voltage suppression for interference frequency f1 in Hz	60 / 50 Hz
Conversion time (per channel)	65 ms ; 55 / 65 ms (additional 20 ms on activated wire-break test)
Smoothing of measured values	
Parameterizable	Yes ; In four stages by means of digital filtering
Step: None	Yes ; 1 x cycle time
Step: low	Yes ; 4 x cycle time
Step: Medium	Yes ; 32 x cycle time
Step: High	Yes ; 64 x cycle time
Encoder	
Connection of signal encoders	
for voltage measurement	Yes
Errors/accuracies	
Linearity error (relative to input area)	+/- 0,01 %
Temperature error (relative to input area)	+/- 0,005 %/K
Crosstalk between the inputs, min.	-50 dB
Repeat accuracy in settled status at 25 °C (relative to input area)	+/- 0,05 %
Operational limit in overall temperature range	
Voltage, relative to input area	+/- 0,6 %
Basic error limit (operational limit at 25 °C)	
Voltage, relative to input area	+/- 0,4 %
Interference voltage suppression for $f = n \times (f_l \pm 1\%)$, f_l = interference frequency	
Series mode interference (peak value of interference < rated value of input range), min.	70 dB
common mode voltage (USS < 2.5 V) , min.	90 dB
Isochronous mode	
Isochronous operation (application synchronized up to terminal)	No
Interrupts/diagnostics/status information	
Diagnostic messages	
Diagnostic functions	Yes ; Can be read out

Diagnostic information readable	Yes
Wire break	Yes ; A break in the wire is only detected for thermocouples
Group error	Yes
Overflow/underflow	Yes
Diagnostics indication LED	
Group error SF (red)	Yes
Parameter	
Diagnosis: wire break	Disable / enable (wire break is detected only in thermocouples)
Measurement type/range	Deactivated/ +/- 80 mV/ TC-EL Type T (Cu-CuNi)/ TC-EL Type K (NiCr-Ni)/ TC-EL Type B (PtRh-PtRh)/ TC-EL Type c (Wer-Wer) TC-EL Type N (NiCrSi-NiSi)/ TC-EL Type E (NiCr-CuNi)/ TC-EL Type R (PtRh-Pt)/ TC-EL Type S (PtRh-Pt)/ TC-EL Type J (Fe-Cu-Ni)/ TC
Group diagnostics	Disable / enable
Overflow/underflow	Disable / enable
Comparison point	none / RTD
Comparison point number	None / 1 / 2 / 3 / 4 / 5 / 6 / 7 / 8
Unit	Celsius
Galvanic isolation	
Galvanic isolation analog inputs	
between the channels	No
between the channels and the backplane bus	Yes
between the channels and the load voltage L+	Yes
Permissible potential difference	
between inputs and MANA (UCM)	2 V AC PP
between MANA and M internally (UIISO)	75 VDC / 60 VAC
Isolation	
Isolation checked with	500 V DC
Dimensions	
Width	15 mm
Height	81 mm
Depth	52 mm
Weight	

Weight, approx.	40 g
Status	Jul 17, 2012