## **SIEMENS**

## **Data sheet**

6ES7677-2FA41-0FL0



Figure similar

SIMATIC ET 200SP Open Controllers, CPU 1515SP PC F +HMI 512PT, 4 GB RAM, 30 GB CFAST with WES 7 P 64 bit pre-installed, mit S7-1500 Fail-safe SWC CPU 1505SP F pre-installed with WinCC Runtime Advanced V14 pre-installed with 512 PowerTags license, Interfaces: 1x slot CFAST, 1x slot SD/MMC, 1x connection for ET 200SP bus adapter PROFINET 1x 10/100/1000 Mbit/s Ethernet, 3x USB, 1x DVI-I graphics card connection, Documentation on DVD, Restore DVD

General information	
Product type designation	CPU 1515SP PC F
HW functional status	FS02
Firmware version	V2.1
Engineering with	
<ul> <li>STEP 7 TIA Portal configurable/integrated from version</li> </ul>	V14 SP1
Installed software	
<ul> <li>Visualization</li> </ul>	WinCC Runtime Advanced V14 SP1
<ul> <li>Control</li> </ul>	S7-1500 Software Controller CPU 1505SP F
Configuration control	
via dataset	Yes
Control elements	
Mode selector switch	1
Supply voltage	
Rated value (DC)	24 V
permissible range, lower limit (DC)	19.2 V
permissible range, upper limit (DC)	28.8 V
Reverse polarity protection	Yes
Mains buffering	
<ul> <li>Mains/voltage failure stored energy time</li> </ul>	5 ms
Input current	
Current consumption (rated value)	1.5 A; Full processor load, incl. ET 200SP modules and using USB
Current consumption (in no-load operation), typ.	0.6 A
Inrush current, max.	4.7 A; Rated value
Power	
Active power input, max.	36 W; incl. ET 200SP modules and using USB
Infeed power to the backplane bus	8.75 W
Power loss	
Power loss, typ.	15 W; without ET 200SP modules and without using USB
Processor	
Processor type	Dual-Core 1 GHz, AMD G Series APU T40E
Memory	
Type of memory	DDR3-SDRAM
Main memory	4 GB RAM
CFast memory card	Yes; 30 GB flash memory
SIMATIC memory card required	No
Work memory	

<ul><li>integrated (for program)</li></ul>	1.5 Mbyte
<ul><li>integrated (for data)</li></ul>	5 Mbyte
<ul> <li>integrated (for CPU function library of CPU Runtime)</li> </ul>	10 Mbyte
Load memory	
integrated (on PC mass storage)	320 Mbyte
Backup	320 Mibyte
• with UPS	Yes; all memory areas declared retentive
with or s      with non-volatile memory	Yes
·	165
CPU processing times	40 mg
for bit operations, typ.	10 ns
for word operations, typ.	12 ns
for fixed point arithmetic, typ.	16 ns
for floating point arithmetic, typ.	64 ns
CPU-blocks	
Number of elements (total)	6 000; In addition to blocks such as DBs, FBs and FCs, UDTs, global constants, etc. are also regarded as elements
DB	
<ul><li>Number, max.</li></ul>	5 999; Number range: 1 to 65535
• Size, max.	5 Mbyte
FB	
<ul><li>Number, max.</li></ul>	5 998; Number range: 1 to 65535
• Size, max.	512 kbyte
FC	
<ul><li>Number, max.</li></ul>	5 999; Number range: 1 to 65535
• Size, max.	512 kbyte
OB	
<ul><li>Size, max.</li></ul>	1 048 kbyte
<ul> <li>Number of free cycle OBs</li> </ul>	100
<ul> <li>Number of time alarm OBs</li> </ul>	20
<ul> <li>Number of delay alarm OBs</li> </ul>	20
<ul> <li>Number of cyclic interrupt OBs</li> </ul>	20
<ul> <li>Number of process alarm OBs</li> </ul>	50
<ul> <li>Number of DPV1 alarm OBs</li> </ul>	3
<ul> <li>Number of isochronous mode OBs</li> </ul>	1
<ul> <li>Number of technology synchronous alarm OBs</li> </ul>	2
<ul> <li>Number of startup OBs</li> </ul>	100
<ul> <li>Number of asynchronous error OBs</li> </ul>	4
<ul> <li>Number of synchronous error OBs</li> </ul>	2
Number of diagnostic alarm OBs	1
Nesting depth	
per priority class	24; Up to 8 possible for F-blocks
Counters, timers and their retentivity	
S7 counter	
Number	2 048
Retentivity	
— adjustable	Yes
IEC counter	
Number	Any (only limited by the main memory)
Retentivity	
— adjustable	Yes
S7 times	
Number	2 048
Retentivity	
— adjustable	Yes
IEC timer	
Number	Any (only limited by the main memory)
Retentivity	, (Sinj minos of the main monory)
— adjustable	Yes
Data areas and their retentivity	
Data areas and their retentivity	

Retentive data area (incl. timers, counters, flags), max.	410 kbyte; For storage in NVRAM; for storage in mass storage 5 242
	020 bytes
Flag	
• Size, max.	16 kbyte
Number of clock memories	8; 8 clock memory bit, grouped into one clock memory byte
Data blocks	
<ul> <li>Retentivity adjustable</li> </ul>	Yes
Retentivity preset	No
Local data	
per priority class, max.	64 kbyte; max. 16 KB per block
Address area	
Number of IO modules	8 192
I/O address area	
<ul><li>Inputs</li></ul>	32 kbyte; All inputs are in the process image
<ul><li>Outputs</li></ul>	32 kbyte; All outputs are in the process image
of which per assigned PC interface	
— Inputs (volume)	8 kbyte
— Outputs (volume)	8 kbyte
Subprocess images	
Number of subprocess images, max.	32
Hardware configuration	
Integrated power supply	Yes
Number of distributed IO systems	20
Number of DP masters	
• Via CM	1
Rack	
Modules per rack, max.	64; CPU 1515SP PC + 64 modules + server module
Number of lines, max.	1
PtP CM	1
Number of PtP CMs	the number of connectable DtD CMs is only limited by the number of
• Number of FtF Civis	the number of connectable PtP CMs is only limited by the number of available slots
Time of day	
Clock	
• Type	Hardware clock
Hardware clock (real-time)	Yes; Resolution: 1 s
Backup time	6 wk; At 40 °C ambient temperature, typically
Deviation per day, max.	10 s; Typ.: 2 s
Clock synchronization	10 0, 19p.: 2 0
• supported	Yes
• to DP, master	No
on Ethernet via NTP	Yes
	Yes
on Windows clock, slave	163
Interfaces	
Number of industrial Ethernet interfaces	2
Number of PROFINET interfaces	1
Number of PROFIBUS interfaces	_ 1
Number of RS 485 interfaces	1; Via CM DP module
Number of USB interfaces	3; 3x USB 2.0 on the front, 500 mA each - of which 2x 500 mA and 1x
Niverbase of OD accedable	100 mA simultaneously
Number of SD card slots	1
Video interfaces	4 DVIII
Graphics interface	1x DVI-I
1. Interface	
Interface type	PROFINET
automatic detection of transmission rate	Yes
Autonegotiation	Yes
Autocrossing	Yes
	100
Number of connections	88

— Transmission rate, max.	100 Mbit/s
<ul> <li>Industrial Ethernet status LED</li> </ul>	Yes
<ul> <li>Number of ports</li> </ul>	2
<ul> <li>integrated switch</li> </ul>	Yes
BusAdapter (PROFINET)	Yes; Applicable BusAdapter: BA 2x RJ45, BA 2x FC
Protocols	
PROFINET IO Controller	Yes
PROFINET IO Device	Yes
SIMATIC communication	Yes
Open IE communication	Yes
Web server	Yes
PROFINET IO Controller	
Services	
— Isochronous mode	Yes
— shortest clock pulse	500 μs
— IRT	Yes
Prioritized startup	Yes; Max. 32 PROFINET devices
Number of connectable IO Devices, max.	128
Of which IO devices with IRT, max.	64
Of which to devices with IRT, max.  — of which in line, max.	64
<ul> <li>Number of connectable IO Devices for RT, max.</li> </ul>	128
— of which in line, max.	128
Number of IO Devices that can be	8
simultaneously activated/deactivated, max.	·
<ul> <li>IO Devices changing during operation (partner ports), supported</li> </ul>	Yes
Number of IO Devices per tool, max.	8
Updating times	The minimum value of the update time also depends on communication
— Opdating times	share set for PROFINET IO, on the number of IO devices, and on the quantity of configured user data
Update time for IRT	
— for send cycle of 500 μs	500 μs to 8 ms
— for send cycle of 1 ms	1 ms to 16 ms
— for send cycle of 2 ms	2 ms to 32 ms
— for send cycle of 4 ms	4 ms to 64 ms
— With IRT and parameterization of "odd" send	Update time = set "odd" send clock (any multiple of 125 µs: 375 µs, 625
	μs 3 875 μs)
cycles	μο ο ο το μο)
cycles Update time for RT	до о ото дој
·	500 μs to 256 ms
Update time for RT	
Update time for RT — for send cycle of 500 μs	500 μs to 256 ms
Update time for RT  — for send cycle of 500 μs  — for send cycle of 1 ms	500 μs to 256 ms 1 ms to 512 ms
Update time for RT  — for send cycle of 500 µs  — for send cycle of 1 ms  — for send cycle of 2 ms	500 μs to 256 ms 1 ms to 512 ms 2 ms to 512 ms
Update time for RT  — for send cycle of 500 µs  — for send cycle of 1 ms  — for send cycle of 2 ms  — for send cycle of 4 ms	500 μs to 256 ms 1 ms to 512 ms 2 ms to 512 ms
Update time for RT  — for send cycle of 500 µs  — for send cycle of 1 ms  — for send cycle of 2 ms  — for send cycle of 4 ms  PROFINET IO Device	500 μs to 256 ms 1 ms to 512 ms 2 ms to 512 ms
Update time for RT  — for send cycle of 500 µs  — for send cycle of 1 ms  — for send cycle of 2 ms  — for send cycle of 4 ms  PROFINET IO Device  Services	500 μs to 256 ms 1 ms to 512 ms 2 ms to 512 ms 4 ms to 512 ms
Update time for RT  — for send cycle of 500 µs  — for send cycle of 1 ms  — for send cycle of 2 ms  — for send cycle of 4 ms  PROFINET IO Device  Services  — Isochronous mode	500 μs to 256 ms 1 ms to 512 ms 2 ms to 512 ms 4 ms to 512 ms No
Update time for RT  — for send cycle of 500 µs  — for send cycle of 1 ms  — for send cycle of 2 ms  — for send cycle of 4 ms  PROFINET IO Device  Services  — Isochronous mode  — IRT	500 μs to 256 ms 1 ms to 512 ms 2 ms to 512 ms 4 ms to 512 ms  No Yes
Update time for RT  — for send cycle of 500 µs  — for send cycle of 1 ms  — for send cycle of 2 ms  — for send cycle of 4 ms  PROFINET IO Device  Services  — Isochronous mode  — IRT  — Prioritized startup	500 μs to 256 ms 1 ms to 512 ms 2 ms to 512 ms 4 ms to 512 ms  No Yes Yes
Update time for RT  — for send cycle of 500 µs  — for send cycle of 1 ms  — for send cycle of 2 ms  — for send cycle of 4 ms  PROFINET IO Device  Services  — Isochronous mode  — IRT  — Prioritized startup  — Shared device	500 μs to 256 ms 1 ms to 512 ms 2 ms to 512 ms 4 ms to 512 ms  No Yes Yes Yes
Update time for RT  — for send cycle of 500 µs  — for send cycle of 1 ms  — for send cycle of 2 ms  — for send cycle of 4 ms  PROFINET IO Device  Services  — Isochronous mode  — IRT  — Prioritized startup  — Shared device  — Number of IO Controllers with shared device,	500 μs to 256 ms 1 ms to 512 ms 2 ms to 512 ms 4 ms to 512 ms  No Yes Yes Yes
Update time for RT  — for send cycle of 500 µs  — for send cycle of 1 ms  — for send cycle of 2 ms  — for send cycle of 4 ms  PROFINET IO Device  Services  — Isochronous mode  — IRT  — Prioritized startup  — Shared device  — Number of IO Controllers with shared device, max.	500 µs to 256 ms 1 ms to 512 ms 2 ms to 512 ms 4 ms to 512 ms  No Yes Yes Yes 4
Update time for RT  — for send cycle of 500 µs  — for send cycle of 1 ms  — for send cycle of 2 ms  — for send cycle of 4 ms  PROFINET IO Device  Services  — Isochronous mode  — IRT  — Prioritized startup  — Shared device  — Number of IO Controllers with shared device, max.  2. Interface	500 μs to 256 ms 1 ms to 512 ms 2 ms to 512 ms 4 ms to 512 ms  No Yes Yes Yes
Update time for RT  — for send cycle of 500 µs  — for send cycle of 1 ms  — for send cycle of 2 ms  — for send cycle of 4 ms  PROFINET IO Device  Services  — Isochronous mode  — IRT  — Prioritized startup  — Shared device  — Number of IO Controllers with shared device, max.  2. Interface  Interface type  automatic detection of transmission rate	500 μs to 256 ms 1 ms to 512 ms 2 ms to 512 ms 4 ms to 512 ms  No Yes Yes Yes 4  Integrated Ethernet interface Yes
Update time for RT  — for send cycle of 500 µs — for send cycle of 1 ms — for send cycle of 2 ms — for send cycle of 4 ms  PROFINET IO Device  Services — Isochronous mode — IRT — Prioritized startup — Shared device — Number of IO Controllers with shared device, max.  2. Interface Interface type automatic detection of transmission rate Autonegotiation	500 μs to 256 ms 1 ms to 512 ms 2 ms to 512 ms 4 ms to 512 ms  No Yes Yes Yes 4  Integrated Ethernet interface Yes Yes
Update time for RT  — for send cycle of 500 µs  — for send cycle of 1 ms  — for send cycle of 2 ms  — for send cycle of 4 ms  PROFINET IO Device  Services  — Isochronous mode  — IRT  — Prioritized startup  — Shared device  — Number of IO Controllers with shared device, max.  2. Interface  Interface type  automatic detection of transmission rate  Autoreossing	500 μs to 256 ms 1 ms to 512 ms 2 ms to 512 ms 4 ms to 512 ms  No Yes Yes Yes 4  Integrated Ethernet interface Yes
Update time for RT  — for send cycle of 500 µs  — for send cycle of 1 ms  — for send cycle of 2 ms  — for send cycle of 4 ms  PROFINET IO Device  Services  — Isochronous mode  — IRT  — Prioritized startup  — Shared device  — Number of IO Controllers with shared device, max.  2. Interface  Interface type  automatic detection of transmission rate  Autorossing  Interface types	500 µs to 256 ms 1 ms to 512 ms 2 ms to 512 ms 4 ms to 512 ms  No Yes Yes Yes 4  Integrated Ethernet interface Yes Yes Yes
Update time for RT  — for send cycle of 500 µs  — for send cycle of 1 ms  — for send cycle of 2 ms  — for send cycle of 4 ms  PROFINET IO Device  Services  — Isochronous mode  — IRT  — Prioritized startup  — Shared device  — Number of IO Controllers with shared device, max.  2. Interface  Interface type  automatic detection of transmission rate  Autocrossing  Interface types  • RJ 45 (Ethernet)	500 µs to 256 ms 1 ms to 512 ms 2 ms to 512 ms 4 ms to 512 ms  No Yes Yes Yes Yes 4  Integrated Ethernet interface Yes Yes Yes Yes Yes Yes
Update time for RT  — for send cycle of 500 µs  — for send cycle of 1 ms  — for send cycle of 2 ms  — for send cycle of 4 ms  PROFINET IO Device  Services  — Isochronous mode  — IRT  — Prioritized startup  — Shared device  — Number of IO Controllers with shared device, max.  2. Interface  Interface type  automatic detection of transmission rate  Autorossing  Interface types  • RJ 45 (Ethernet)  — Transmission rate, max.	500 µs to 256 ms 1 ms to 512 ms 2 ms to 512 ms 4 ms to 512 ms  No Yes Yes Yes Yes 4  Integrated Ethernet interface Yes Yes Yes Yes Yes Yes Yes Yes Yes Ye
Update time for RT  — for send cycle of 500 µs  — for send cycle of 1 ms  — for send cycle of 2 ms  — for send cycle of 4 ms  PROFINET IO Device  Services  — Isochronous mode  — IRT  — Prioritized startup  — Shared device  — Number of IO Controllers with shared device, max.  2. Interface  Interface type  automatic detection of transmission rate  Autocrossing  Interface types  • RJ 45 (Ethernet)	500 µs to 256 ms 1 ms to 512 ms 2 ms to 512 ms 4 ms to 512 ms  No Yes Yes Yes Yes 4  Integrated Ethernet interface Yes Yes Yes Yes Yes Yes

3. Interface	
Interface type	PROFIBUS with CM DP
Number of connections	44
Interface types	
• RS 485	Yes
Protocols	100
PROFIBUS DP master	Yes
PROFIBUS DP slave	Yes
SIMATIC communication	Yes
PROFIBUS DP master	
Number of DP slaves, max.	125
Services	
— Equidistance	No
— Isochronous mode	No
Interface types	
RS 485	
Transmission rate, max.	12 Mbit/s
Protocols	
PROFIsafe	Yes; V2.4 / V2.6
Number of connections	100, 12.17 12.0
Number of connections, max.	88
Number of connections reserved for ES/HMI/web	10
Number of S7 routing paths	16
Redundancy mode	10
Media redundancy	
— MRP	Yes
— MRPD	Yes
Switchover time on line break, typ.	200 ms
Number of stations in the ring, max.	50
SIMATIC communication	
PG/OP communication	Yes
• S7 routing	Yes
S7 communication, as server	Yes
S7 communication, as client	Yes
User data per job, max.	64 kbyte
Open IE communication	
• TCP/IP	Yes
— Data length, max.	64 kbyte
• ISO-on-TCP (RFC1006)	Yes
— Data length, max.	64 kbyte
• UDP	Yes
— Data length, max.	1 472 kbyte
• SNMP	Yes
• DCP	Yes
• LLDP	Yes
Web server	
• HTTP	Yes; Via Windows and PROFINET interface
• HTTPS	Yes; Only via PROFINET interface
OPC UA	
OPC UA Client	No
OPC UA Server	Yes; Data access (read, write, subscribe), runtime license required
<ul> <li>Application authentication</li> </ul>	Yes; Available security policies: None, Basic128Rsa15, Basic256Rsa15, Basic256Sha256
— Security policies	Yes; Available security policies: None, Basic128Rsa15, Basic256Rsa15, Basic256Sha256
— User authentication	Yes; "anonymous" or by user name & password
Further protocols  • MODBUS	Yes; MODBUS TCP
S7 message functions	
Number of login stations for message functions, max.	32
<u> </u>	

Drogram clarms	Voc
Program alarms	Yes 10,000
Number of configurable program messages, max.	10 000
Number of simultaneously active program alarms	
Number of program alarms	1 000
<ul> <li>Number of alarms for system diagnostics</li> </ul>	200
Number of alarms for motion technology objects	160
Test commissioning functions	
Joint commission (Team Engineering)	Yes; Parallel online access possible for up to 8 engineering systems
Status block	Yes; up to 8 simultaneously
Single step	No
Status/control	
Status/control variable	Yes
<ul><li>Variables</li></ul>	Inputs, outputs, memory bits, DB, times, counters
<ul> <li>Number of variables, max.</li> </ul>	
<ul><li>of which status variables, max.</li></ul>	200
— of which control variables, max.	200
Forcing	
• Forcing	Yes
Forcing, variables	Inputs, outputs
Number of variables, max.	200
Diagnostic buffer	
• present	Yes
•	1 000
Number of entries, max.  of which powerful proof.	300
— of which powerfail-proof	300
Traces	4
Number of configurable Traces	4
Memory size per trace, max.	512 kbyte
Interrupts/diagnostics/status information	
Diagnostics indication LED	
RUN/STOP LED	Yes
• ERROR LED	Yes
MAINT LED	Yes
Supported technology objects	
Motion Control	Yes
<ul> <li>Number of available Motion Control resources for technology objects</li> </ul>	2 400
<ul> <li>Required Motion Control resources</li> </ul>	
<ul> <li>per speed-controlled axis</li> </ul>	40; per axis
— per positioning axis	80; per axis
— per synchronous axis	
	160; per axis
<ul> <li>per external encoder</li> </ul>	160; per axis 80; per external encoder
•	80; per external encoder
— per output cam	80; per external encoder 20; per cam
— per output cam  — per cam track	80; per external encoder 20; per cam 160; per cam track
<ul><li>per output cam</li><li>per cam track</li><li>per probe</li></ul>	80; per external encoder 20; per cam
<ul> <li>per output cam</li> <li>per cam track</li> <li>per probe</li> <li>Positioning axis</li> <li>Number of positioning axes at motion control</li> </ul>	80; per external encoder 20; per cam 160; per cam track
<ul> <li>per output cam</li> <li>per cam track</li> <li>per probe</li> <li>Positioning axis</li> <li>Number of positioning axes at motion control cycle of 4 ms (typical value)</li> <li>Number of positioning axes at motion control</li> </ul>	80; per external encoder 20; per cam 160; per cam track 40; per probe
<ul> <li>per output cam</li> <li>per cam track</li> <li>per probe</li> <li>Positioning axis</li> <li>Number of positioning axes at motion control cycle of 4 ms (typical value)</li> <li>Number of positioning axes at motion control cycle of 8 ms (typical value)</li> </ul>	80; per external encoder 20; per cam 160; per cam track 40; per probe
— per output cam  — per cam track  — per probe  • Positioning axis  — Number of positioning axes at motion control cycle of 4 ms (typical value)  — Number of positioning axes at motion control cycle of 8 ms (typical value)  Controller	80; per external encoder 20; per cam 160; per cam track 40; per probe 5
— per output cam  — per cam track  — per probe  • Positioning axis  — Number of positioning axes at motion control cycle of 4 ms (typical value)  — Number of positioning axes at motion control cycle of 8 ms (typical value)  Controller  • PID_Compact	80; per external encoder 20; per cam 160; per cam track 40; per probe  5 12  Yes; Universal PID controller with integrated optimization
— per output cam  — per cam track  — per probe  • Positioning axis  — Number of positioning axes at motion control cycle of 4 ms (typical value)  — Number of positioning axes at motion control cycle of 8 ms (typical value)  Controller  • PID_Compact  • PID_3Step	80; per external encoder 20; per cam 160; per cam track 40; per probe  5 12  Yes; Universal PID controller with integrated optimization Yes; PID controller with integrated optimization for valves
<ul> <li>per output cam</li> <li>per cam track</li> <li>per probe</li> <li>Positioning axis</li> <li>Number of positioning axes at motion control cycle of 4 ms (typical value)</li> <li>Number of positioning axes at motion control cycle of 8 ms (typical value)</li> </ul> Controller <ul> <li>PID_Compact</li> <li>PID_3Step</li> <li>PID-Temp</li> </ul>	80; per external encoder 20; per cam 160; per cam track 40; per probe  5 12  Yes; Universal PID controller with integrated optimization
— per output cam  — per cam track  — per probe  • Positioning axis  — Number of positioning axes at motion control cycle of 4 ms (typical value)  — Number of positioning axes at motion control cycle of 8 ms (typical value)  Controller  • PID_Compact  • PID_3Step  • PID-Temp  Counting and measuring	80; per external encoder 20; per cam 160; per cam track 40; per probe  5 12  Yes; Universal PID controller with integrated optimization Yes; PID controller with integrated optimization for valves Yes; PID controller with integrated optimization for temperature
— per output cam  — per cam track  — per probe  • Positioning axis  — Number of positioning axes at motion control cycle of 4 ms (typical value)  — Number of positioning axes at motion control cycle of 8 ms (typical value)  Controller  • PID_Compact  • PID_3Step  • PID-Temp  Counting and measuring  • High-speed counter	80; per external encoder 20; per cam 160; per cam track 40; per probe  5 12  Yes; Universal PID controller with integrated optimization Yes; PID controller with integrated optimization for valves
— per output cam  — per cam track  — per probe  • Positioning axis  — Number of positioning axes at motion control cycle of 4 ms (typical value)  — Number of positioning axes at motion control cycle of 8 ms (typical value)  Controller  • PID_Compact  • PID_3Step  • PID-Temp  Counting and measuring	80; per external encoder 20; per cam 160; per cam track 40; per probe  5 12  Yes; Universal PID controller with integrated optimization Yes; PID controller with integrated optimization for valves Yes; PID controller with integrated optimization for temperature
— per output cam  — per cam track  — per probe  • Positioning axis  — Number of positioning axes at motion control cycle of 4 ms (typical value)  — Number of positioning axes at motion control cycle of 8 ms (typical value)  Controller  • PID_Compact  • PID_3Step  • PID-Temp  Counting and measuring  • High-speed counter	80; per external encoder 20; per cam 160; per cam track 40; per probe  5 12  Yes; Universal PID controller with integrated optimization Yes; PID controller with integrated optimization for valves Yes; PID controller with integrated optimization for temperature
— per output cam  — per cam track  — per probe  • Positioning axis  — Number of positioning axes at motion control cycle of 4 ms (typical value)  — Number of positioning axes at motion control cycle of 8 ms (typical value)  Controller  • PID_Compact  • PID_Astep  • PID-Temp  Counting and measuring  • High-speed counter  Standards, approvals, certificates	80; per external encoder 20; per cam 160; per cam track 40; per probe  5 12  Yes; Universal PID controller with integrated optimization Yes; PID controller with integrated optimization for valves Yes; PID controller with integrated optimization for temperature
— per output cam  — per cam track  — per probe  • Positioning axis  — Number of positioning axes at motion control cycle of 4 ms (typical value)  — Number of positioning axes at motion control cycle of 8 ms (typical value)  Controller  • PID_Compact  • PID_3Step  • PID-Temp  Counting and measuring  • High-speed counter  Standards, approvals, certificates  CE mark	80; per external encoder 20; per cam 160; per cam track 40; per probe  5 12  Yes; Universal PID controller with integrated optimization Yes; PID controller with integrated optimization for valves Yes; PID controller with integrated optimization for temperature  Yes

DCM (formarly C TICV)	Von
RCM (formerly C-TICK)	Yes
Highest safety class achievable in safety mode	Dia
Performance level according to ISO 13849-1     SUL and to ISO 64508	PLe
SIL acc. to IEC 61508  Probability of failure /for convice life of 20 years and rep	SIL 3
Probability of failure (for service life of 20 years and rep	·
<ul> <li>Low demand mode: PFDavg in accordance with SIL3</li> </ul>	< 2.00E-05
<ul> <li>High demand/continuous mode: PFH in accordance with SIL3</li> </ul>	< 1.00E-09 1/h
Ambient conditions	
Ambient temperature during operation	
• min.	0 °C
• max.	Up to 60 $^{\circ}$ C with max. 32 ET 200SP modules and 3x 100 mA USB load; up to 55 $^{\circ}$ C with max. 64 ET 200SP modules and 2x max. 500 mA and 1x max. 100 mA USB load
<ul><li>horizontal installation, min.</li></ul>	0 °C
<ul> <li>horizontal installation, max.</li> </ul>	60 °C
<ul> <li>vertical installation, min.</li> </ul>	0 °C
vertical installation, max.	50 °C; With max. 32 ET 200SP modules and 3x 100 mA USB load
Ambient temperature during storage/transportation	
• min.	-40 °C
• max.	70 °C
Vibrations	
<ul> <li>Operation, tested according to IEC 60068-2-6</li> </ul>	Yes
Transport, tested acc. to IEC 60068-2-6	Yes
Shock testing	
<ul> <li>tested according to IEC 60068-2-6</li> </ul>	Yes
<ul> <li>tested according to IEC 60068-2-27</li> </ul>	Yes
<ul><li>tested according to IEC 60068-2-29</li></ul>	Yes
• Storage/transport, tested acc. to IEC 60068-2-27	Yes
Operating systems	
pre-installed operating system	Windows Embedded Standard 7 P 64-bit
pro-installed operating system	Wildows Embedded Standard 7 F 04-bit
configuration / header	Williams Embedded Standard 7 P 04-bit
	Williams Ellipedded Stalldard 7 P 04-bit
configuration / header	Williams Embedded Standard 7 F 04-bit
configuration / header configuration / programming / header	
configuration / header configuration / programming / header Programming language	Yes; incl. failsafe
configuration / header configuration / programming / header Programming language — LAD — FBD	
configuration / header configuration / programming / header Programming language — LAD	Yes; incl. failsafe Yes; incl. failsafe
configuration / header configuration / programming / header Programming language — LAD — FBD — STL — SCL	Yes; incl. failsafe Yes; incl. failsafe Yes Yes
configuration / header configuration / programming / header Programming language — LAD — FBD — STL — SCL — CFC	Yes; incl. failsafe Yes; incl. failsafe Yes Yes Yes No
configuration / header configuration / programming / header Programming language — LAD — FBD — STL — SCL — CFC — GRAPH	Yes; incl. failsafe Yes; incl. failsafe Yes Yes
configuration / header configuration / programming / header Programming language — LAD — FBD — STL — SCL — CFC — GRAPH Know-how protection	Yes; incl. failsafe Yes; incl. failsafe Yes Yes Yes No Yes
configuration / header configuration / programming / header  Programming language — LAD — FBD — STL — SCL — CFC — GRAPH  Know-how protection  • User program protection/password protection	Yes; incl. failsafe Yes; incl. failsafe Yes Yes Yes No Yes
configuration / header  configuration / programming / header  Programming language  — LAD  — FBD  — STL  — SCL  — CFC  — GRAPH  Know-how protection  • User program protection/password protection • Copy protection	Yes; incl. failsafe Yes; incl. failsafe Yes Yes Yes No Yes  Yes Yes
configuration / header  configuration / programming / header  Programming language  — LAD  — FBD  — STL  — SCL  — CFC  — GRAPH  Know-how protection  • User program protection/password protection  • Copy protection  • Block protection	Yes; incl. failsafe Yes; incl. failsafe Yes Yes Yes No Yes
configuration / header  configuration / programming / header  Programming language  — LAD  — FBD  — STL  — SCL  — CFC  — GRAPH  Know-how protection  • User program protection/password protection  • Copy protection  • Block protection  Access protection	Yes; incl. failsafe Yes; incl. failsafe Yes Yes Yes No Yes  Yes Yes Yes Yes Yes
configuration / header  configuration / programming / header  Programming language  — LAD  — FBD  — STL  — SCL  — CFC  — GRAPH  Know-how protection  • User program protection/password protection  • Copy protection  • Block protection  Access protection  • Protection level: Write protection	Yes; incl. failsafe Yes; incl. failsafe Yes Yes Yes No Yes  Yes Yes Yes Yes Yes Yes
configuration / header  configuration / programming / header  Programming language  — LAD  — FBD  — STL  — SCL  — CFC  — GRAPH  Know-how protection  • User program protection/password protection • Copy protection  • Block protection  Access protection  • Protection level: Write protection  • Protection level: Read/write protection	Yes; incl. failsafe Yes; incl. failsafe Yes Yes No Yes  Yes Yes Yes Yes Yes Yes Yes
configuration / header  configuration / programming / header  Programming language  — LAD  — FBD  — STL  — SCL  — CFC  — GRAPH  Know-how protection  • User program protection/password protection • Copy protection • Block protection  Access protection  • Protection level: Write protection • Protection level: Read/write protection • Protection level: Complete protection	Yes; incl. failsafe Yes; incl. failsafe Yes Yes Yes No Yes  Yes Yes Yes Yes Yes Yes
configuration / header  configuration / programming / header  Programming language  — LAD  — FBD  — STL  — SCL  — CFC  — GRAPH  Know-how protection  • User program protection/password protection • Copy protection • Block protection  Access protection  • Protection level: Write protection • Protection level: Read/write protection  • Protection level: Complete protection  programming / cycle time monitoring / header	Yes; incl. failsafe Yes; incl. failsafe Yes Yes No Yes  Yes Yes Yes Yes Yes Yes Yes Yes Ye
configuration / header  configuration / programming / header  Programming language  — LAD  — FBD  — STL  — SCL  — CFC  — GRAPH  Know-how protection  • User program protection/password protection  • Copy protection  • Block protection  • Block protection  • Protection level: Write protection  • Protection level: Complete protection  programming / cycle time monitoring / header  • lower limit	Yes; incl. failsafe Yes; incl. failsafe Yes Yes No Yes  Yes Yes Yes Yes Yes Yes Yes Yes Adjustable minimum cycle time
configuration / header  configuration / programming / header  Programming language  — LAD  — FBD  — STL  — SCL  — CFC  — GRAPH  Know-how protection  • User program protection/password protection  • Copy protection  • Block protection  Access protection  • Protection level: Write protection  • Protection level: Read/write protection  • Protection level: Complete protection  programming / cycle time monitoring / header  • lower limit  • upper limit	Yes; incl. failsafe Yes; incl. failsafe Yes Yes No Yes  Yes Yes Yes Yes Yes Yes Yes Yes Ye
configuration / header  configuration / programming / header  Programming language  — LAD  — FBD  — STL  — SCL  — CFC  — GRAPH  Know-how protection  • User program protection/password protection • Copy protection • Block protection  Access protection  • Protection level: Write protection • Protection level: Read/write protection • Protection level: Complete protection  programming / cycle time monitoring / header • lower limit • upper limit  Open Development interfaces	Yes; incl. failsafe Yes; incl. failsafe Yes Yes Yes No Yes  Yes Yes Yes Yes Yes Yes Yes Adjustable minimum cycle time adjustable maximum cycle time
configuration / header  configuration / programming / header  Programming language  — LAD  — FBD  — STL  — SCL  — CFC  — GRAPH  Know-how protection  • User program protection/password protection • Copy protection • Block protection  Access protection  • Protection level: Write protection  • Protection level: Read/write protection  • Protection level: Complete protection  programming / cycle time monitoring / header  • lower limit  • upper limit  Open Development interfaces  • Size of ODK SO file, max.	Yes; incl. failsafe Yes; incl. failsafe Yes Yes No Yes  Yes Yes Yes Yes Yes Yes Yes Yes Adjustable minimum cycle time
configuration / header  configuration / programming / header  Programming language  — LAD  — FBD  — STL  — SCL  — CFC  — GRAPH  Know-how protection  • User program protection/password protection  • Copy protection  • Block protection  • Block protection  Access protection  • Protection level: Write protection  • Protection level: Read/write protection  • Protection level: Complete protection  programming / cycle time monitoring / header  • lower limit  • upper limit  Open Development interfaces  • Size of ODK SO file, max.  Peripherals/Options	Yes; incl. failsafe Yes; incl. failsafe Yes Yes Yes No Yes  Yes Yes Yes Yes Yes Yes Yes Adjustable minimum cycle time adjustable maximum cycle time 3.8 Mbyte
configuration / header  configuration / programming / header  Programming language  — LAD  — FBD  — STL  — SCL  — CFC  — GRAPH  Know-how protection  • User program protection/password protection • Copy protection • Block protection  Access protection  • Protection level: Write protection  • Protection level: Read/write protection  • Protection level: Complete protection  programming / cycle time monitoring / header  • lower limit  • upper limit  Open Development interfaces  • Size of ODK SO file, max.	Yes; incl. failsafe Yes; incl. failsafe Yes Yes Yes No Yes  Yes Yes Yes Yes Yes Yes Yes Adjustable minimum cycle time adjustable maximum cycle time
configuration / header  configuration / programming / header  Programming language  — LAD  — FBD  — STL  — SCL  — CFC  — GRAPH  Know-how protection  • User program protection/password protection  • Copy protection  • Block protection  • Block protection  Access protection  • Protection level: Write protection  • Protection level: Read/write protection  • Protection level: Complete protection  programming / cycle time monitoring / header  • lower limit  • upper limit  Open Development interfaces  • Size of ODK SO file, max.  Peripherals/Options	Yes; incl. failsafe Yes; incl. failsafe Yes Yes Yes No Yes  Yes Yes Yes Yes Yes Yes Yes Adjustable minimum cycle time adjustable maximum cycle time 3.8 Mbyte
configuration / header  configuration / programming / header  Programming language  — LAD  — FBD  — STL  — SCL  — CFC  — GRAPH  Know-how protection  • User program protection/password protection  • Copy protection  • Block protection  Access protection  • Protection level: Write protection  • Protection level: Read/write protection  • Protection level: Complete protection  programming / cycle time monitoring / header  • lower limit  • upper limit  Open Development interfaces  • Size of ODK SO file, max.  Peripherals/Options  SD card	Yes; incl. failsafe Yes; incl. failsafe Yes Yes Yes No Yes  Yes Yes Yes Yes Yes Yes Yes Adjustable minimum cycle time adjustable maximum cycle time 3.8 Mbyte
configuration / header  configuration / programming / header  Programming language  — LAD  — FBD  — STL  — SCL  — CFC  — GRAPH  Know-how protection  • User program protection/password protection • Copy protection • Block protection  • Block protection  • Protection level: Write protection • Protection level: Read/write protection • Protection level: Complete protection  • Protection level: Complete protection  programming / cycle time monitoring / header  • lower limit • upper limit  Open Development interfaces • Size of ODK SO file, max.  Peripherals/Options  SD card  Dimensions	Yes; incl. failsafe Yes; incl. failsafe Yes Yes No Yes  Yes Yes Yes Yes Yes Yes Yes  Yes
configuration / header  configuration / programming / header  Programming language  — LAD  — FBD  — STL  — SCL  — CFC  — GRAPH  Know-how protection  • User program protection/password protection • Copy protection • Block protection  • Protection level: Write protection  • Protection level: Read/write protection  • Protection level: Complete protection  • Protection level: Complete protection  programming / cycle time monitoring / header  • lower limit  • upper limit  Open Development interfaces  • Size of ODK SO file, max.  Peripherals/Options  SD card  Dimensions  Width	Yes; incl. failsafe Yes; incl. failsafe Yes Yes No Yes  Yes Yes Yes Yes Yes Yes  Yes Yes  Yes Yes

Weights 0.83 kg Weight, approx. 11/3/2021

last modified: